

**REMARKS**

At the outset, the Examiner is thanked for the thorough review and consideration of the pending application. The Office Action dated August 13, 2008 has been received and its contents carefully reviewed.

Claims 1-5, 7-11, 13-18 and 20 are rejected. Applicant has amended claims 1, 9 and 15, and cancelled claim 10 to further define the invention. No new matter has been added.

The Office Action rejects claims 1-5, 7-11, 13-18 and 20 under 35 U.S.C. 103(a) as being unpatentable over AAPA in view of Suzuki et al. (US 5,598,180), hereinafter "Suzuki", and further in view of Maekawa et al. (US 5,764,207), hereinafter Maekawa.

Reexamination and reconsideration of the pending claims are respectfully requested.

Claim 1 is allowable over the cited references in that claim 1 recites a combination of elements including, for example, "wherein the dummy pixel signals applicable to adjacent one of the data lines have voltage levels identical to each other and have polarities different from each other, during the blanking period of every frame, wherein the dummy pixel signals are applied before the data lines are induced into a floating state during the blanking period".

Claim 9 is allowable over the cited references in that claim 9 recites a combination of elements including, for example, "wherein the dummy pixel signals applied to the data lines each have substantially the same voltage levels and wherein the dummy pixel signals applied to adjacent data lines have different polarities during the blanking period of every frame, wherein the dummy pixel signals are applied before the data lines are induced into a floating state during the blanking period".

Claim 15 is allowable over the cited references in that claim 15 recites a combination of elements including, for example, "floating the plurality of data lines within the LCD panel during the blanking period, wherein the dummy pixel signals applicable to adjacent one of the data lines have voltage levels identical to each other and have polarities different from each other, during the blanking period of every frame, wherein the dummy pixel signals are applied before the data lines are induced into a floating state during the blanking period.

In Suzuki, after not applying gate signals, white-level potential is applied.

In the meantime, potential levels Vp1 and Vp2 of Maekawa are applied during blanking period of every one horizontal period. In general, one frame is a period for displaying all pixels

of LCD panel. On the contrary, one horizontal period is a period for displaying pixels on one gate line of LCD panel.

In addition, in Maekawa, potential level Vp1 is applied during blanking period of one horizontal period, and potential level Vp2 is applied during blanking period of another horizontal period.

None of the cited references, singly or in combination, teaches or suggests at least this feature of the claimed invention. Accordingly, Applicant respectfully submits that claims 1-5, 7-9, 11, 13-18 and 20, which depend therefrom, are allowable over the cited references.

Applicants believe the foregoing amendments and remarks place the application in condition for allowance and early, favorable action is respectfully solicited.

If for any reason the Examiner finds the application other than in condition for allowance, the Examiner is requested to call the undersigned attorney at (202) 496-7500 to discuss the steps necessary for placing the application in condition for allowance. All correspondence should continue to be sent to the below-listed address.

If these papers are not considered timely filed by the Patent and Trademark Office, then a petition is hereby made under 37 C.F.R. §1.136, and any additional fees required under 37 C.F.R. §1.136 for any necessary extension of time, or any other fees required to complete the filing of this response, may be charged to Deposit Account No. 50-0911. Please credit any overpayment to deposit Account No. 50-0911.

Dated: November 12, 2008

Respectfully submitted,

By Valerie P. Hayes  
Valerie P. Hayes  
Registration No.: 53.005  
McKENNA LONG & ALDRIDGE LLP  
1900 K Street, N.W.  
Washington, DC 20006  
(202) 496-7500  
Attorneys for Applicant